

Serial No. 10/824,363
Amdt. dated August 4, 2006
Reply to Office Action of May 8, 2006

Docket No. K-0634

Amendments to the Specification

Please amend Paragraph [0008] as follows:

Referring to FIG. 1, an indium-tin-oxide (ITO) strip 102 is linearly aligned in a strip form on a glass substrate 101. Subsequently, a counter electrode 103 having a width narrower than the ITO strip 102 is formed on the ITO strip 102. And, an organic electroluminous (EL) layer 104 formed of a hole transport layer, an emission layer, and an electron transport layer serially deposited thereon, is formed over the ITO strip 102. An insulating layer 106 is formed between the ITO strip 102 and a barrier rib. A cathode strip 105 formed in the shape of a strip overlaps the ITO strip 102 [[12]] above the organic EL layer 104. Then, a barrier rib 107 formed in the shape of a strip is formed between each cathode strip 105, so as to separate the cathode strips 105 adjacent to one another. Finally, the organic EL display panel is completed when the substrate having the cathode strips 105 formed thereon is paired with a seal-cover 109 by using a sealant 108.

Please amend Paragraph [0037] as follows:

FIGs. 5A to 5F illustrate perspective views of the process steps for fabricating an organic electroluminescence display panel according to the present invention, and FIG. 5G shows a process step for forming an insulating layer in accordance with an alternative embodiment;

Please amend Paragraph [0046] as follows:

More specifically, as shown in FIG. 6, the counter electrode 513 can be formed of one of or a combination of at least two (2) of a polygon, a cross, and a circle, in the form of a grid. However, the grid patterns should be spaced apart at an optimum distance “b”, so as to prevent the adjacent grid patterns from contacting one another. Preferably, the distance “a” between each shape in the grid pattern should also be greater than zero (0).

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Please amend Paragraph [0047] as follows:

Subsequently, the insulating layer 516 is formed, as shown in FIG. 5C. Herein, insulating layer 516 does not extend into an area where the counter electrode overlaps the sealant. In accordance with an alternative embodiment shown in FIG. 5G the insulating layer 516 is extended to a predetermined region including the region whereby the counter electrode 513 overlaps (e.g., crosses) the sealant, and also to a portion of the glass substrate 511, so as to be formed at a peripheral region of the organic EL layer, which is formed in a later process. Moreover, referring to FIG. 5D, a barrier rib 517 is formed to electrically insulate the cathode strips, which are also formed in a later process.

Please amend Paragraph [0051] as follows:

Herein, the counter electrode 513 according to the present invention is formed to have a width smaller than that of the ITO strip 512. In other words, the width of the ITO strip 102, 202, and 512, shown in FIGs. 7A to 7C, is uniform[[],]. However however, the width of the respective counter electrodes 203 and 513 of FIGs. 7B and 7C is the smallest. Moreover, as shown in FIG. 7C, the counter electrode 513 512 is formed in a grid form, thereby reducing line resistance.